



Term	Plaintiff's Proposed Construction	Defendants' Proposed Construction	Court's Final Construction
<p>“main memory”</p> <p>U.S Patent No. 8,683,296, Cls. 1–5, 7, 10–12, 14–17, 23–27, 29–32, &amp; 34–39; U.S Patent No. 9,160,374, Cls. 1, 5–7, 11–13, &amp; 17–18; U.S Patent No. 9,385,759, Cls. 1, 5–8, 12–15, &amp; 19–20; U.S Patent No. 10,003,358, Cls. 1–4, 11–16, 19–23, 31–35, 38–42, 50–54, &amp; 57; U.S Patent No. 10,291,259, Cls. 1–4, 12–16, 19–23, 31–35, 38–42, 50–54, &amp; 57; U.S Patent No. 10,666,296, Cls. 1-8</p> <p>Proposed by Defendants</p>	<p>Plain and ordinary meaning</p>	<p>“part of volatile internal storage into which instructions and other data must be loaded for subsequent execution or processing (e.g., RAM) not including registers or cache”</p>	<p>Plain-and-ordinary meaning wherein the plain-and-ordinary meaning is “the memory between the last-level cache and non-volatile storage medium (<i>e.g.</i>, HDD, etc.).”</p>

Term	Plaintiff's Proposed Construction	Defendants' Proposed Construction	Court's Final Construction
<p>“thread” / “threads”</p> <p>U.S Patent No. 8,683,296, Cls. 1–5, 7, 10–12, &amp; 14–17; U.S Patent No. 9,160,374, Cls. 1 &amp; 5–6; U.S Patent No. 9,385,759, Cls. 1 &amp; 5–7; U.S Patent No. 10,003,358, Cls. 1–4, 11–16 &amp; 19; U.S Patent No. 10,291,259, Cls. 1–4, 12–16 &amp; 19; U.S Patent No. 10,666,296, Cls. 1–8</p> <p>Proposed by Defendants</p>	<p>Plain and ordinary meaning</p>	<p>“a single flow of control within a process”</p>	<p>Plain-and-ordinary meaning</p>